

Notice of References Cited	Application/Control No. 10/645,045		Applicant(s)/Patent Under Reexamination BOOTH ET AL.	
	Examiner Hari Kunamneni		Art Unit 2109	Page 1 of <u>1</u>

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*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
*	A	US-6,094,439	07-2000	Krishna et al.	370/445
*	B	US-2005/0013311	01-2005	Samudrala et al.	370/412
	C	US-			
	D	US-			
	F	US-			
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	G	US-			
	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
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NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Geoff Waters, 10 Gigabit Ethernet and the XAUI interface, January 2002, Agilent Technologies, Pages 5, 8, 11, and 12
	V	Applicaition Note, Implementing an 8b/10b Encoder/Decoder for Gigabit Ethernet in the Actel SX FPGA Family, October 1998, Actel Corporation, Page 9
	W	Dave Fifield, GMII Electrical Specification, Jan. 1997, IEEE Interim Meeting, page 12 and 13
	X	Kimmo Pulakka, Optimization of the utilization of a Packet Switched Backbone Network by Using a Control system for Low Priority Traffic, 2000, IEEE

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
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NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	NIST, NIST definition of state machine
	V	
	W	
	X	

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